

# 150mA, 75V Surface Mount Switching Diode

#### **FEATURES**

- Low power loss, high efficiency
- High surge current capability
- Hermetically sealed glass
- RoHS Compliant

# **APPLICATIONS**

- Switching mode power supply (SMPS)
- Adapters
- Lighting application
- On-board DC/DC converter

#### **MECHANICAL DATA**

Case: QMMELF

• Terminal: Matte tin plated leads, solderable per J-STD-002

• Polarity: Indicated by cathode band • Weight: 30.80mg (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I <sub>F</sub>	150	mA		
$V_{RRM}$	75	V		
I <sub>FSM</sub>	4	Α		
$V_F$ at $I_F$ = 100mA	1	V		
T <sub>J MAX</sub>	175	°C		
Package	QMMELF			
Configuration	Single die			







ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)				
PARAMETER		SYMBOL	VALUE	UNIT
Power dissipation		$P_{D}$	500	W
Repetitive peak reverse voltage		$V_{RRM}$	75	V
Forward current		l <sub>F</sub>	150	mA
Non-repetitive peak forward surge current	t = 1s	I <sub>FSM</sub>	0.5	А
	t = 1ms		1.0	А
	t = 1µs		4.0	А
Junction temperature range	·	T <sub>J</sub>	-65 to +175	°C
Storage temperature range		T <sub>STG</sub>	-65 to +175	°C

THERMAL PERFORMANCE				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-ambient thermal resistance	R <sub>OJA</sub>	300	°C/W	



ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage <sup>(1)</sup>	LS4448 LS914B	I <sub>F</sub> = 5mA, T <sub>J</sub> = 25°C	V <sub>F</sub>	-	0.72	V
	LS4148	I <sub>F</sub> = 10mA, T <sub>J</sub> = 25°C		-	1	V
	LS4448 LS914B	I <sub>F</sub> = 100mA, T <sub>J</sub> = 25°C		-	1	V
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>		$V_R = 20V T_J = 25^{\circ}C$	ı	-	25	nA
		$V_R = 75V T_J = 25^{\circ}C$	- I <sub>R</sub>	-	5	μA
Junction capacitance		$1MHz, V_R = 0V$	CJ	-	4	pF

## Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

PRDERING INFORMATION			
ORDERING CODE	PACKAGE	PACKING	
LS4148 L0G	QMMELF	10,000 / 13" Tape & Reel	
LS444B L0G	QMMELF	10,000 / 13" Tape & Reel	
LS914B L0G	QMMELF	10,000 / 13" Tape & Reel	



# **CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25°C unless otherwise noted)

Fig.1 Typical Forward Characteristics

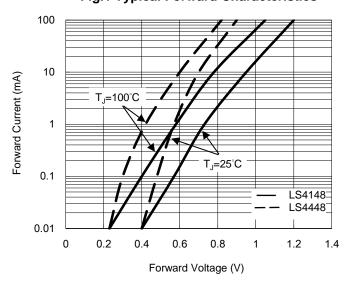


Fig.2 Reverse Current VS. Reverse Voltage

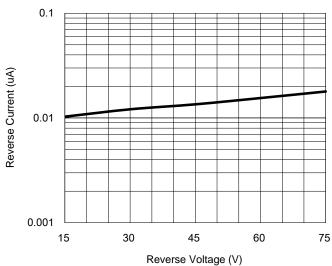


Fig.3 Admissible Power Dissipation Curve

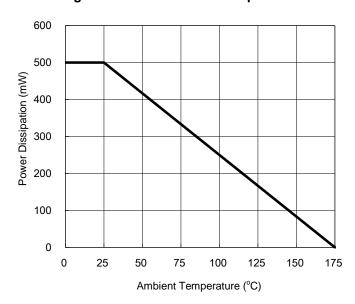
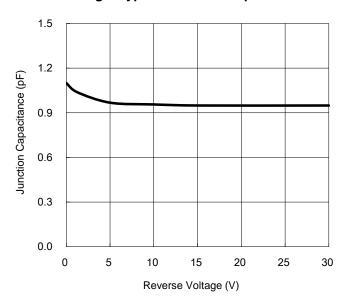


Fig.4 Typical Junction Capacitance

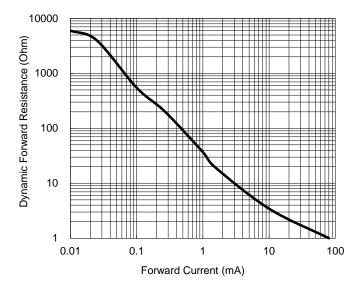




# **CHARACTERISTICS CURVES**

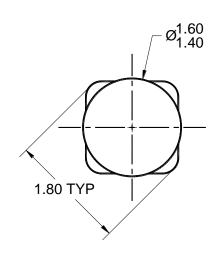
 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

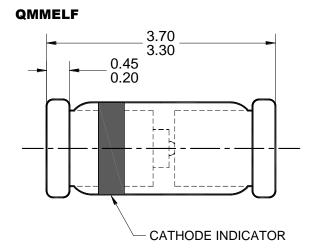
Fig.5 Forward Resistance VS. Forward Current

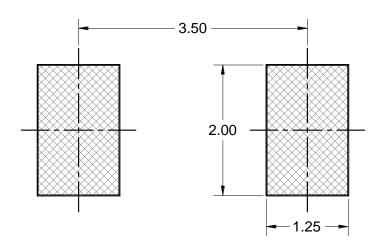




# **PACKAGE OUTLINE DIMENSIONS**







#### NOTES: UNLESS OTHERWISE SPECIFIED

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 3. PACKAGE OUTLINE REFERENCE: JEDEC DO-213, VARIATION AA, ISSUE D.
- 4. DWG NO. REF: HQ2SD07-QMMELF-061 REV A.

SUGGESTED PAD LAYOUT



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